AMENDMENT TO THE SPECIFICATION

Please amend paragraph [0001] as shown below:

Cross reference to related applications

[0001] This application claims the benefit of U.S. provisional Patent Application Serial Number 60/398,943 filed July 25, 2002 for a "Modulation Doped Molecular-Scale Address Decoding" by Andre' DeHon, Patrick Lincoln, U.S. provisional Patent Application Serial Number 60/400,394 filed August 1, 2002 for a "Implementation of Computation Note 15: Integration Issues for Modulation Doped Memory" by Andre' DeHon, Patrick Lincoln, U.S. provisional Patent Application Serial Number 60/415,176 filed September 30, 2002 for "Nanoscale Architectures based on Modulation Doping" by Andre' DeHon, Patrick Lincoln, Charles Lieber, U.S. provisional Patent Application Serial Number 60/429,010 filed November 25, 2002 for "Stochastic Assembly of Sublithographic Nanoscale Interfaces" by Andre' DeHon, Patrick Lincoln, John E. Savage, U.S. provisional Patent Application Serial Number 60/441,995 filed January 23, 2003 for "Stochastic Assembly of Sublithographic Nanoscale Interfaces" by Andre' DeHon, Charles Lieber, Patrick Lincoln, U.S. provisional Patent Application Attorney Docket No. CIT-3877-P, serial number not yet assigned, filed April 25, 2003 for "Sublithographic Nanoscale 3D Architectures" by Andre' DeHon, and U.S. provisional Patent Application Attorney Docket No. CIT-3880-P, serial number not yet assigned, filed May 2, 2003 for "Computing with Electronic Nanotechnologies" by John E. Savage, Andre' DeHon, Patrick Lincoln, Lee-Ad Gottlieb, Arkady Yerukhimovich, the disclosure of all of which is incorporated herein by reference. Also incorporated by reference is the disclosure of U.S. Patent Application Attorney Docket No. 620801-2, serial number ~~not yet assigned~~ 10/627,405, filed on the same day of the present application for a "Stochastic Assembly of Sublithographic Nanoscale Interfaces" by John E. Savage, Andre' DeHon, Patrick Lincoln, and Charles Lieber.